

# Compal Confidential

## EH7L4 MB Schematic Document

**LA-J151P**

**Rev: 1.0**

**2019.06.18b**

Security Classification	Compal Secret Data			Compal Electronics, Inc.		
Issued Date	2019/03/21	Deciphered Date	2019/03/21	Title	Cover Sheet	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number	Rev
				Custom	EH7L4 M/B LA-J151P	0.1
				Date:	Wednesday, June 19, 2019	Sheet 1 of 46

HDMI Conn.



page 22

DDI2  
HDMI x 4 lanes

eDP



page 21

eDP

DDI

Interleaved Memory

260pin DDR4-SO-DIMM X1



page 19

DDR4-ON BOARD 4G 8GbX16



page 20

Memory BUS

Dual Channel

1.2V DDR4 2400(GLK)

Intel  
Gemini Lake  
BGA 1090 balls  
25 x 24 mm  
TDP: 6W  
SDP:4.8W

USB 3.0  
conn x1  
USB3 port 0  
USB2 port 0



page 29

USB 2.0  
conn x2  
USB2 port2 (MB)  
USB2 port3(SUB)



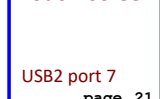
page 29

CMOS  
Camera  
USB2 port 6



page 21

Touch Screen



USB2 port 7  
page 21

USBx8 48MHz

HD Audio 1.8V 24MHz

SPI

SPI ROM  
64Mb page 8

HDA Codec  
ALC255 page 25

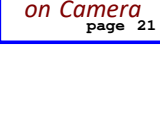
FP

USB2 port 5  
page 31

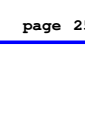
Int. Speaker  
page 25



Int. DMIC  
on Camera  
page 21



UAI  
page 25



M2 SSD  
PCIe/SATA BOM option



page 24

SATA Gen 3  
6.0 Gb/s  
Port1

PCIe 2.0 x2  
5GT/s  
Port 4-5

SATA Gen 3  
6.0 Gb/s  
port 0

SATA HDD  
Conn.



page 26

SATA Gen 1  
1.5 Gb/s  
port 1  
(SATA1A)

USBtoSATA  
Converter IC  
ASM1153  
page 27

SATA ODD  
Conn.  
page 27

LPC/eSPI BUS

CLK=24MHz

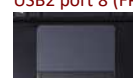
ENE  
KB9022  
page 30

Int.KBD



page 31

Touch Pad  
PS2 (from EC) / I2C (from SOC)  
USB2 port 8 (FP)



page 31

page 06-15

NGFF  
WLAN  
support CNVi  
USB2 port 10



PCIe 1.0  
2.5GT/s  
port 3  
page 24

LAN(GbE)  
Realtek 8111H  
page 23



RJ45 conn.

RTC CKT.

page 9

Power On/Off CKT.

page 33

DC/DC Interface CKT.

page 34

Power Circuit DC/DC

page

Fan Control

page 33

Sub Board

LS-H781P  
IO/B

page 32

LS-H783P  
LID/B

page 32

Security Classification	Compal Secret Data			Compal Electronics, Inc.		
Issued Date	2019/03/21	Deciphered Date	2019/03/21	Title Block Diagrams		
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size Custom	Document Number EH7L4 M/B LA-J151P	Rev 0.1
				Date: Wednesday, June 19, 2019	Sheet 2	of 46

<b>Vcc</b>	<b>3.3V +/- 5%</b>					
<b>Ra</b>	<b>100K +/- 1%</b>					
<b>Board ID</b>	<b>Rb</b>	<b>V<sub>BID</sub> min</b>	<b>V<sub>BID</sub> typ</b>	<b>V<sub>BID</sub> max</b>	<b>EC AD3</b>	<b>PCB Revision</b>
<b>0</b>	<b>0</b>	<b>0 V</b>	<b>0 V</b>	<b>0.300 V</b>	<b>0x00 - 0x13</b>	<b>0.1 (EVT)</b>
<b>1</b>	<b>12K +/- 1%</b>	<b>0.347 V</b>	<b>0.345 V</b>	<b>0.360 V</b>	<b>0x14 - 0x1E</b>	<b>1.0 (DVT)</b>
<b>2</b>	<b>15K +/- 1%</b>	<b>0.423 V</b>	<b>0.430 V</b>	<b>0.438 V</b>	<b>0x1F - 0x25</b>	
<b>3</b>	<b>20K +/- 1%</b>	<b>0.541 V</b>	<b>0.550 V</b>	<b>0.559 V</b>	<b>0x26 - 0x30</b>	
<b>4</b>	<b>27K +/- 1%</b>	<b>0.691 V</b>	<b>0.702 V</b>	<b>0.713 V</b>	<b>0x31 - 0x3A</b>	
<b>5</b>	<b>33K +/- 1%</b>	<b>0.807 V</b>	<b>0.819 V</b>	<b>0.831 V</b>	<b>0x3B - 0x45</b>	
<b>6</b>	<b>43K +/- 1%</b>	<b>0.978 V</b>	<b>0.992 V</b>	<b>1.006 V</b>	<b>0x46 - 0x54</b>	
<b>7</b>	<b>56K +/- 1%</b>	<b>1.169 V</b>	<b>1.185 V</b>	<b>1.200 V</b>	<b>0x55 - 0x64</b>	

BOM Option Table	
Item	BOM Structure
Unpop	@
Connector	CONN@
Test point	TP@
CODEC	255@/256@
EC Mode Select	LPC@ / ESPI@
CNVi	CNVi@
EMI requirement	EMI@ / @EMI@
ESD requirement	ESD@ / @ESD@
RF requirement	RF@ / @RF@
TPM	TPM@
Finger Print	FP@/FPMEC@
SATA HDD	RD@/NRD@
ODD select	USB2SATA@/ODD@
ESD reserve	XEMC@

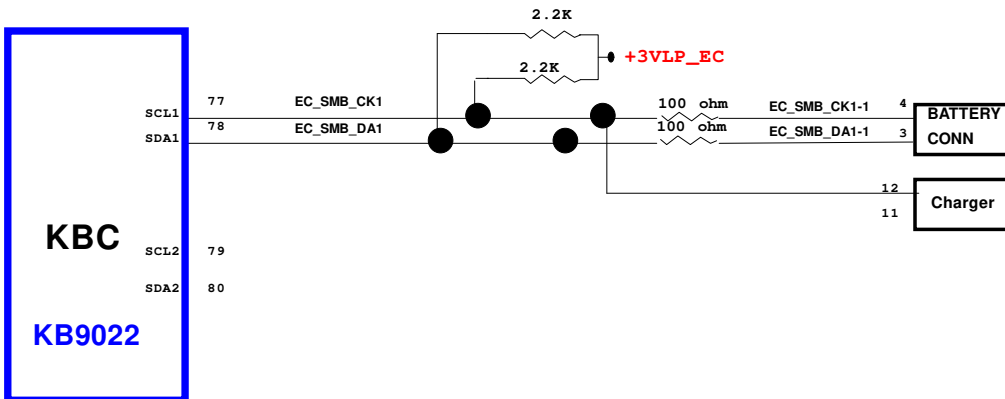
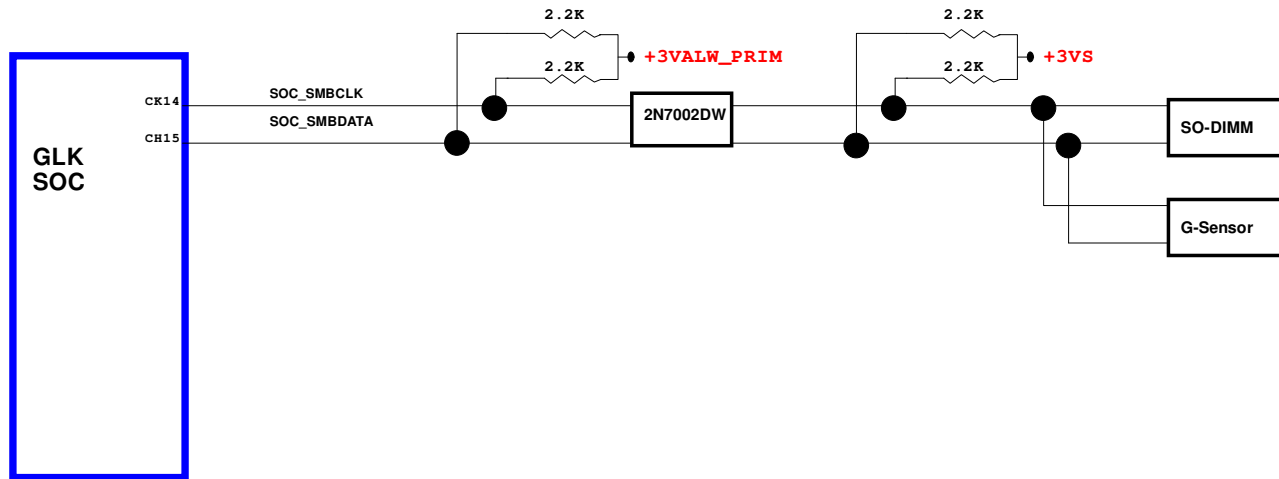
BOM Option Table	
Item	BOM Structure
MB Stage	EVT@/DVT@/PVT@/MP@
G Sensor	GSEN@
Memory related	MD@/NMD@
MD BOM Select	X76@/SDP@/DDP@/
BOM select	15@/17@
4 MIC	4MIC@

[illegible]

STATE \ SIGNAL	SLP_S3#	SLP_S4#	SLP_S5#	+VAL#	+V	+VS	Clock
S0 (Full ON)	HIGH	HIGH	HIGH	ON	ON	ON	ON
S3 (Suspend to RAM)	LOW	HIGH	HIGH	ON	ON	OFF	OFF
S4 (Suspend to Disk)	LOW	LOW	HIGH	ON	OFF	OFF	OFF
S5 (Soft OFF)	LOW	LOW	LOW	ON	OFF	OFF	OFF

[illegible][illegible]

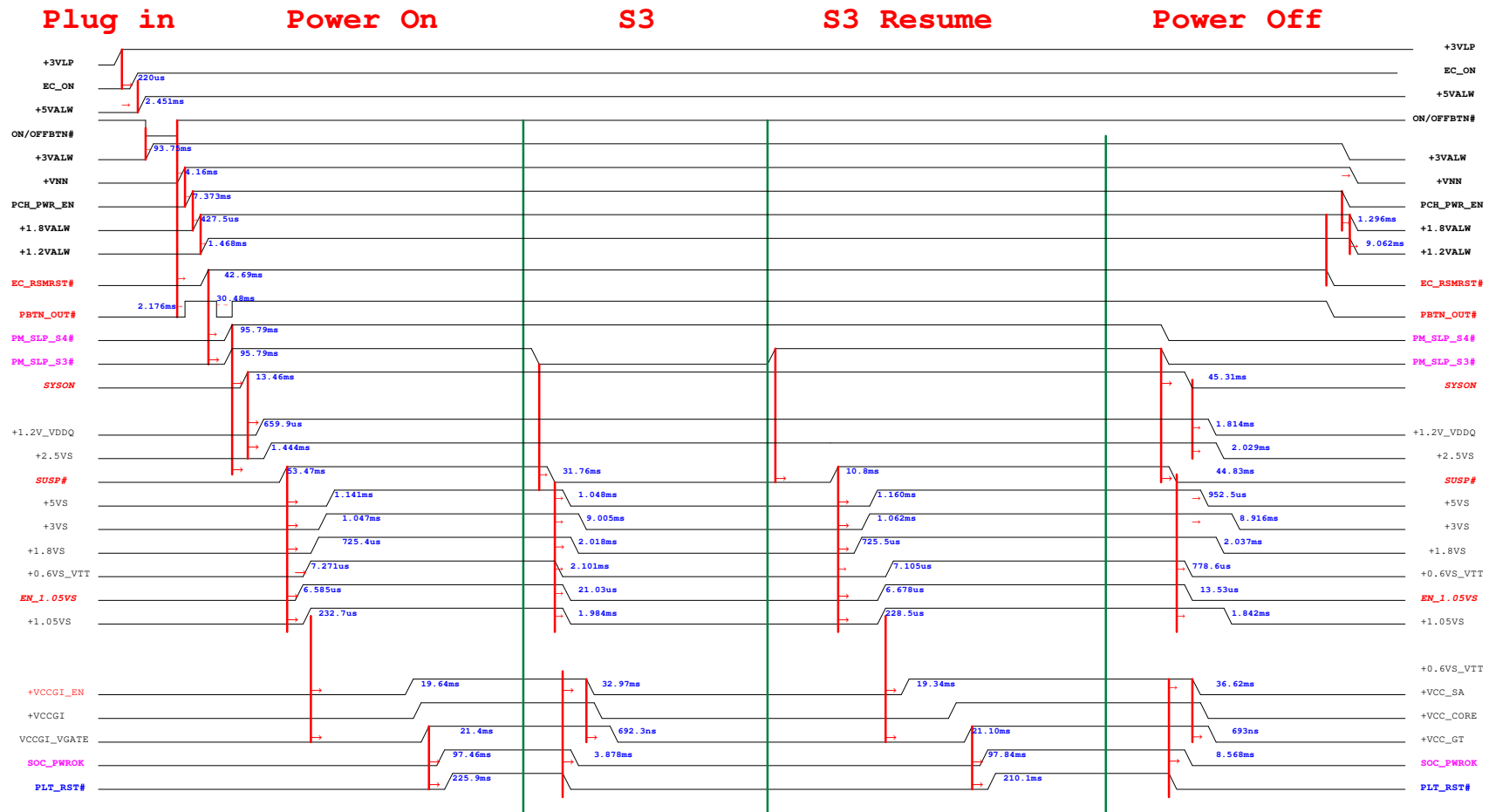
Security Classification	Compal Secret Data			<b>Compal Electronics, Inc.</b> <b>Notes List</b>		
Issued Date	2019/03/21	Deciphered Date	2019/03/21	Title		
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number	Rev
				Custor	<b>EH7LA M/B LA-J151P</b>	0.1
				Date:	Wednesday, June 19, 2019	Sheet 3 of 46



## I2C Address Table

BUS	Device	Address (8 bit)
I2C_0 (+3VS)	Reserved	
I2C_1 (+3VALW_PRIM)	TM-P3393-003 (TP)	0x2C
	FA577E-1206 (TP-ELAN)	0x15
	SA577C-12A0 (TP-ELAN)	0x15
SOC_SMBCLK (+3VS)	SO-DIMM2	0xA4
	G-Sensor	0x30
SOC_SML1CLK (+3VALW_PRIM)	EC	
EC_SMB_CK1 (+3VLP)	BQ24781 (Charger IC)	0x12
	BATTERY PACK	0x16

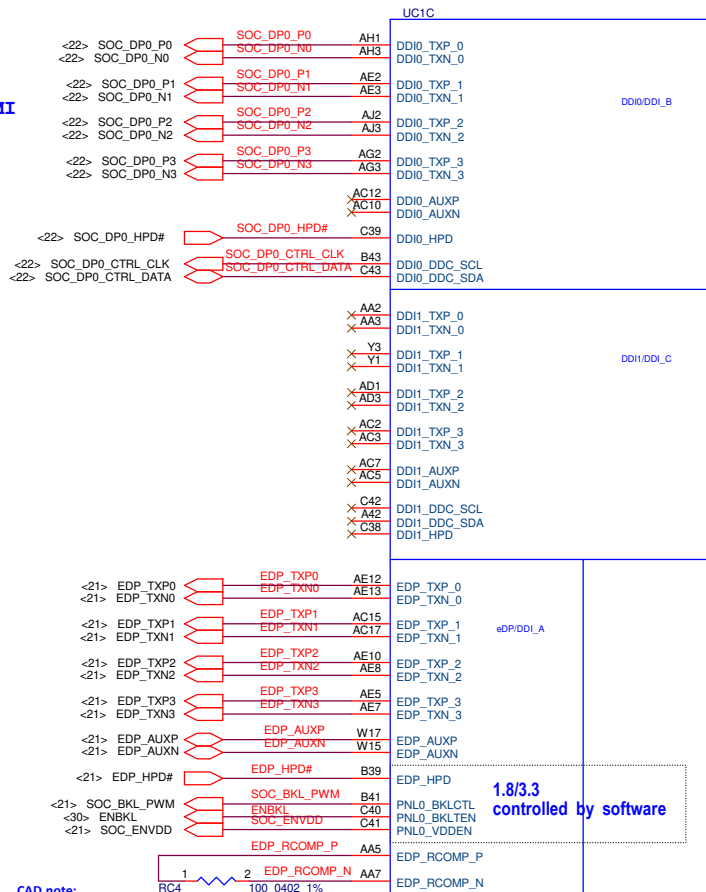
Security Classification	Compal Secret Data			Compal Electronics, Inc.		
Issued Date	2019/03/21	Deciphered Date	2019/03/21	Title	SMBUS Routing Table	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number	Rev
				Custom	EH71A M/B LA-J151P	0.1
				Date:	Wednesday, June 19, 2019	Sheet 5 of 46



Security Classification	Compal Secret Data			Compal Electronics, Inc.	
Issued Date	2017/12/18	Deciphered Date	2018/09/01	Title	Power Sequence
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSMITTED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Rev	1.0
Date	Wednesday, June 18, 2019	Sheet	5	of	67

<eDP>

HDMI



<EC>



To Enable ME Override

ME\_EN

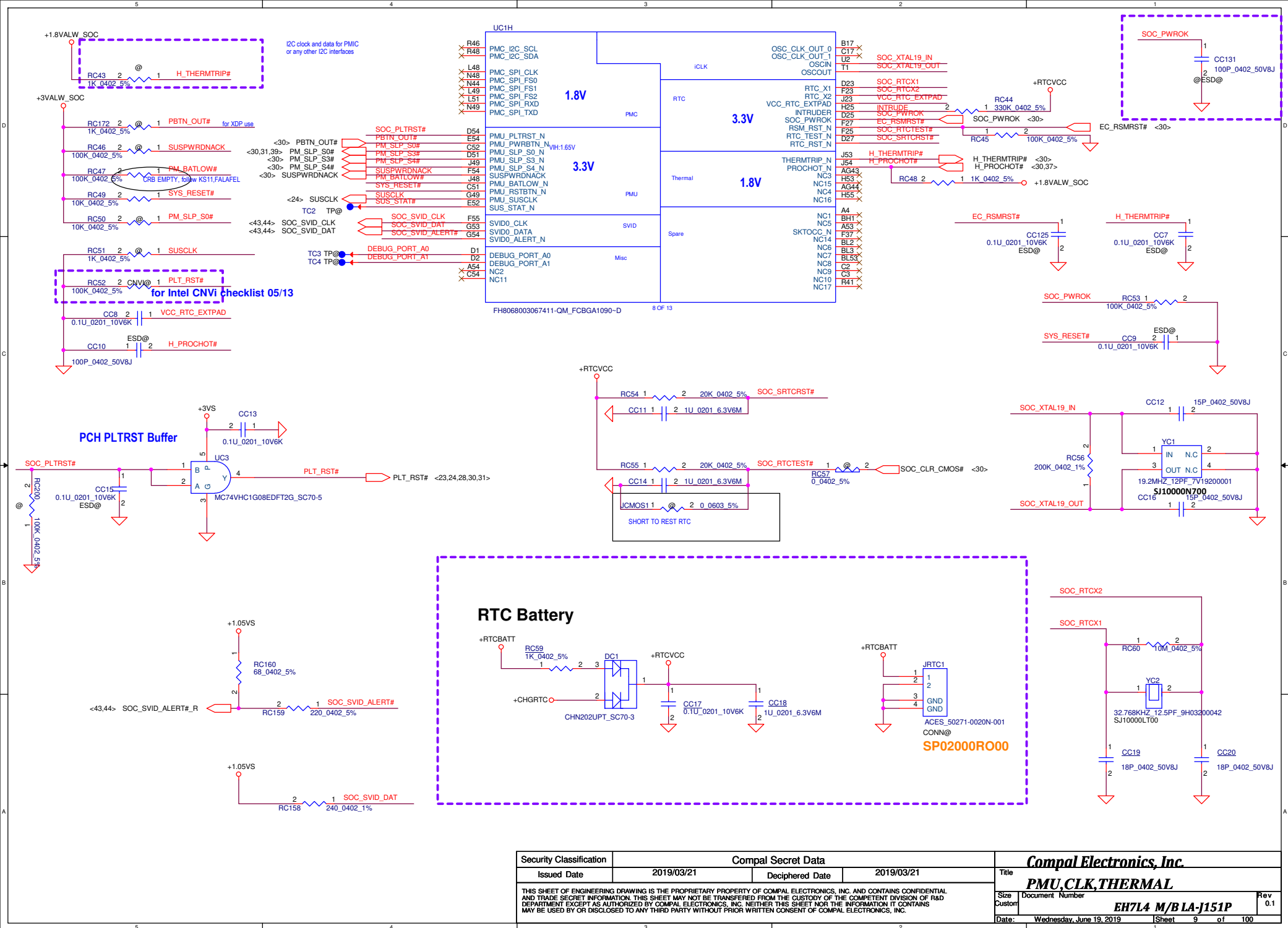
0= No Override(Normal), 1=Override (GPIO\_42)

Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2019/03/21	Deciphered Date	2019/03/21	Title	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				DDI,EDP	
				Size Document Number	Rev
				Custom	0.1
				EH7L4 M/B LA-J151P	
				Date: Wednesday, June 19, 2019	Sheet 6 of 100









Security Classification		Compal Secret Data		<b>Compal Electronics, Inc.</b> <b>PMU, CLK, THERMAL</b>	
Issued Date	2019/03/21	Deciphered Date	2019/03/21	Title	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DETERMINED EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Rev
				Customer	0.1
				Document Number <b>EH714 M/B LA-J151P</b>	
				Date:	
				Wednesday, June 19, 2019	Sheet 9 of 100





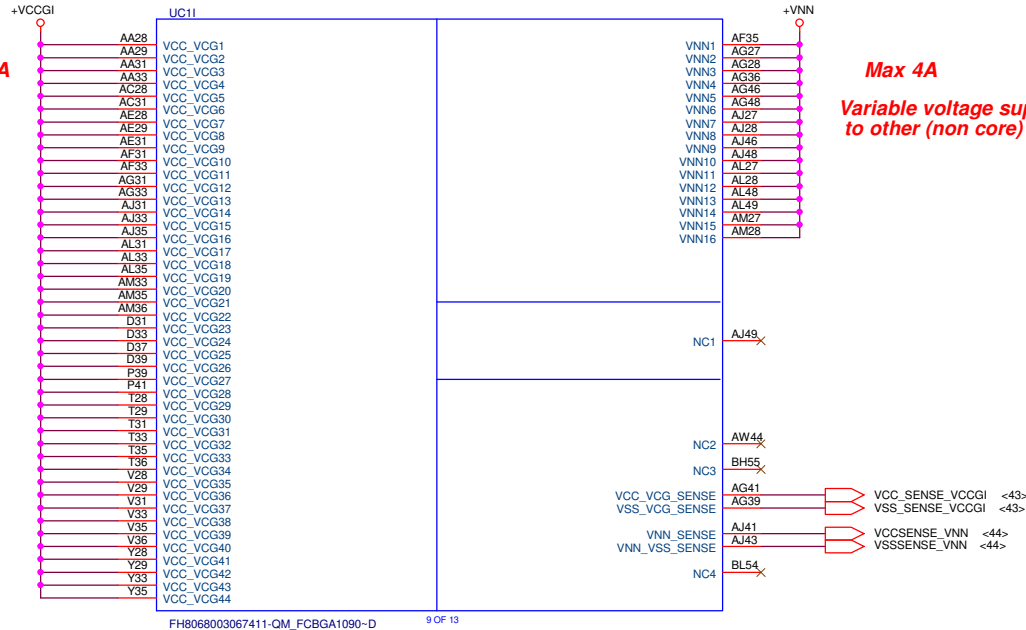


PPVAR\_VCCGI, PPVAR\_VNN, PPVAR\_VCCRAM\_SRAM  
Off at S0ix

Off at S0ix

Variable voltage supply to CPU  
and Graphics Core and ISP  
logic. SVID and I2C VID are  
voltage control interface  
supported.

Max 25A



Max 4A

Variable voltage supply  
to other (non core) logic

FH8068003067411-QM\_FCBGA1090-D

9 OF 13

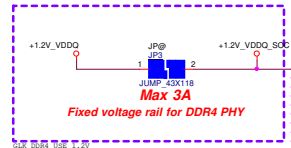
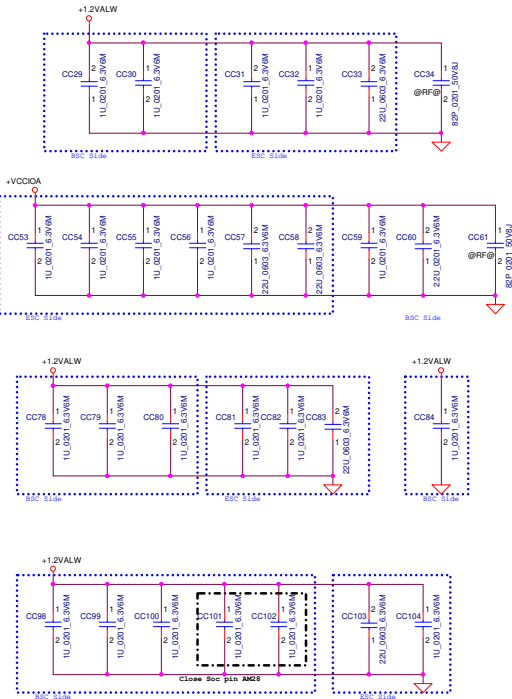
Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2019/03/21	Deciphered Date	2019/03/21	Title	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Power	
Size		Document Number		Rev	
Custom		EH7L4 M/B LA-J151P		0.1	
Date:		Wednesday, June 19, 2019		Sheet 13 of 100	

**PP1200\_A**  
**Max 1.93A**

A-Step only support +1.2V

**Max 2.7A**  
**off at SOIx**

VCC\_IP2V\_A1,A2 (AA18,AA20 pin)  
is only for MIPI-CSI or MIPI-DSI function.



**Max 1.5A**

**Max 0.4A**  
**Fixed voltage rail for all GPIOs**

**Max 2A**  
**Fixed voltage rail for MPHY Logic**

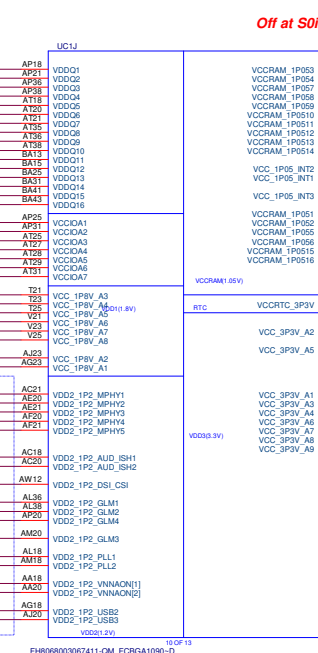
**Fixed voltage rail for Audio and ISH I/O Logic**

**Fixed voltage rail for MIPI I/Os**

**Fixed voltage rail for SoC L2**

**Fixed voltage rail for PLLs**

**Fixed voltage rail for USB2 I/O**

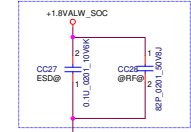
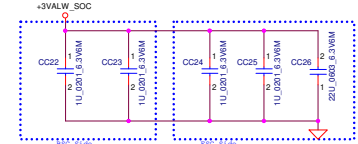


**Off at SOIx**

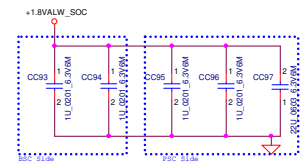
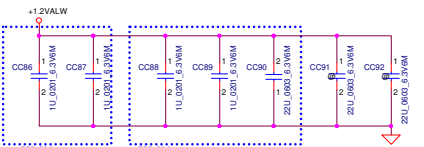
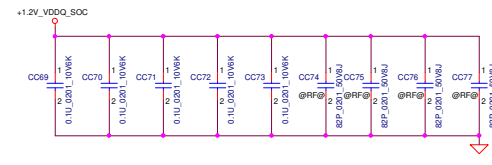
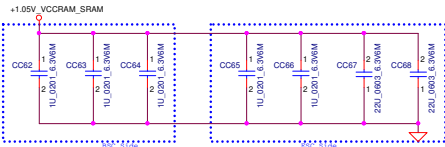
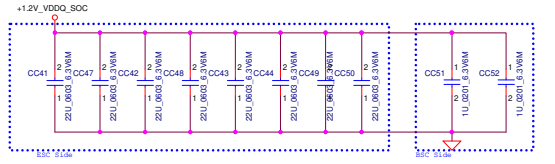
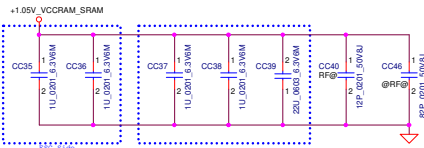
**Max 4.5A**  
**Fixed voltage rail for SRAM and I/O Logic**

**Max 6uA**  
**Fixed Voltage rail for RTC**

**Max 0.15A**  
**Fixed voltage rail for GPIO, I/O logic, and USB2 PHY**



Place near to UC1 as close as possible.



Security Classification		Compal Secret Data		Title	
Issued Date	2019/03/21	Deciphered Date	2019/03/21	Rev	0.1
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSMITTED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Power	
Document Number				EH714 M/BLA-J151P	
Date				Wednesday, June 19, 2019	
Sheet				14 of 100	



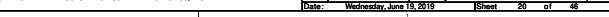






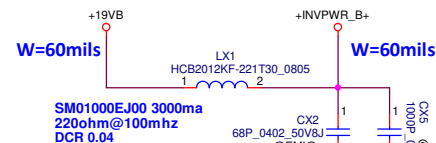
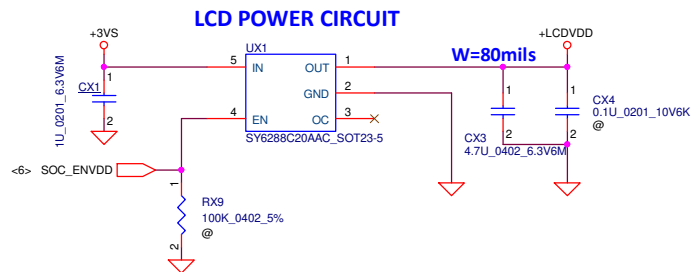




[illegible]

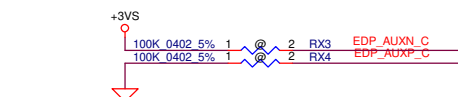
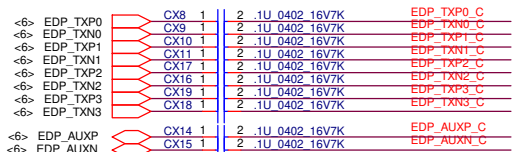
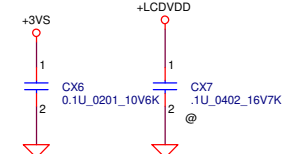
0419 modify for GLK

Security Classification		Compal Secret Data		Title	
Issued Date	2019/03/21	Deciphered Date	2019/03/21		
THE SHEET OF ENGINEERING DRAWINGS IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND/OR SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSMITTED FROM THE CUSTOMER TO ANY OTHER PERSON OR ENTITY WITHOUT THE WRITTEN CONSENT OF COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT THE WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size Customer	
				DDR4 ON BOARD CHIPS	
				E7H1A M/B/LA-151P	
				Date	Wednesday, Jan 13, 2019
				Sheet	20 of 48

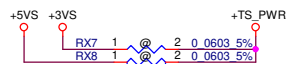


Note: Unmount LX1 when panel boost circuit was use. (2S battery cell)

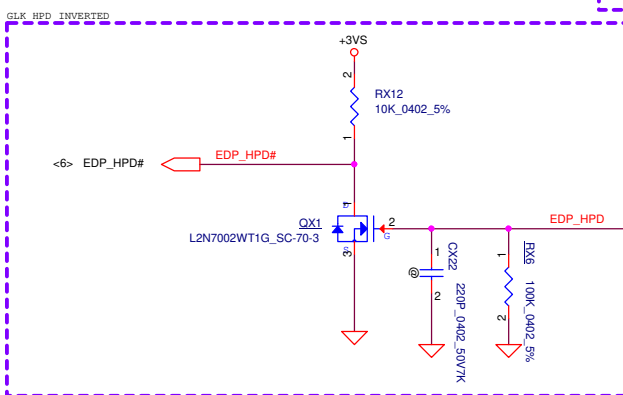
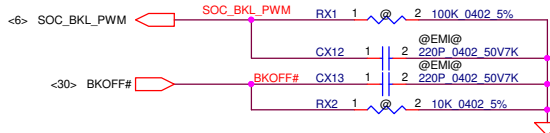
Place closed to JEDP1



### Touch Screen



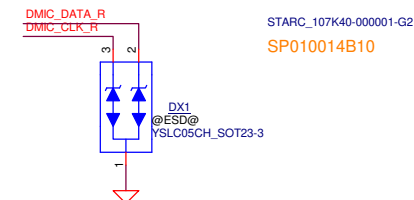
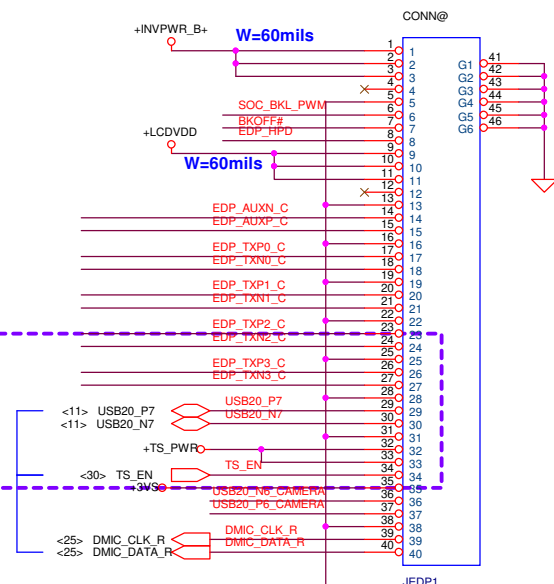
### Camera



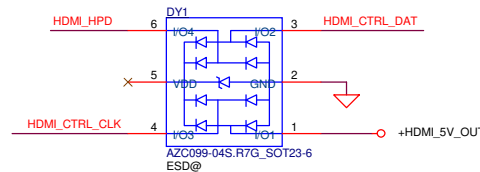
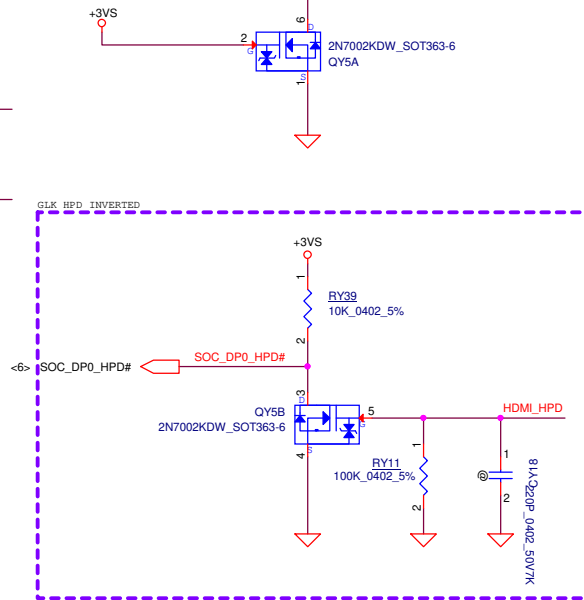
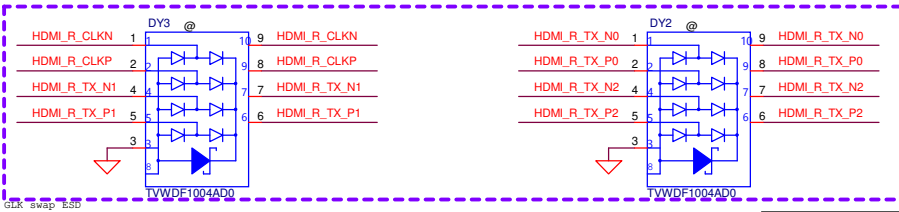
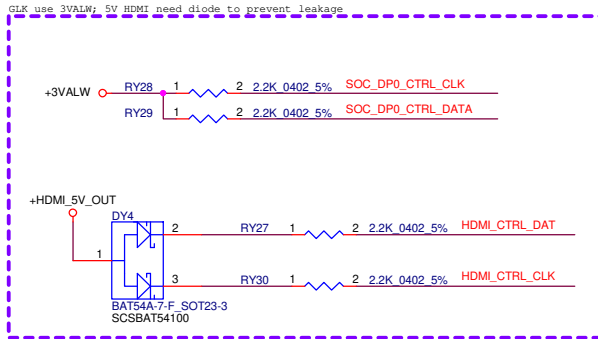
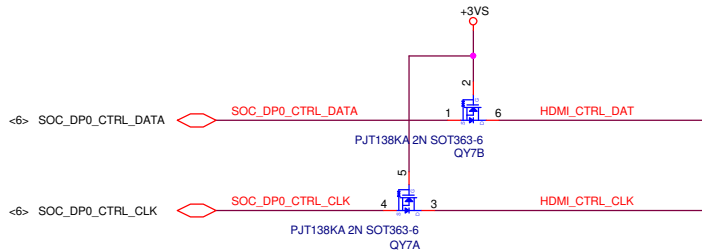
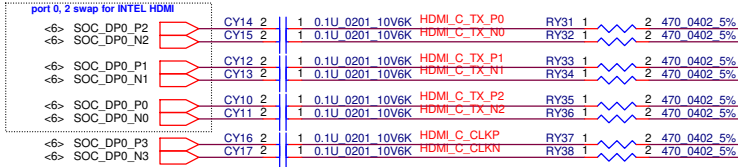
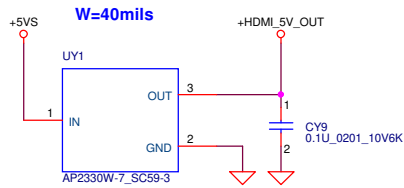
GLK check TS or FP

Touch Screen

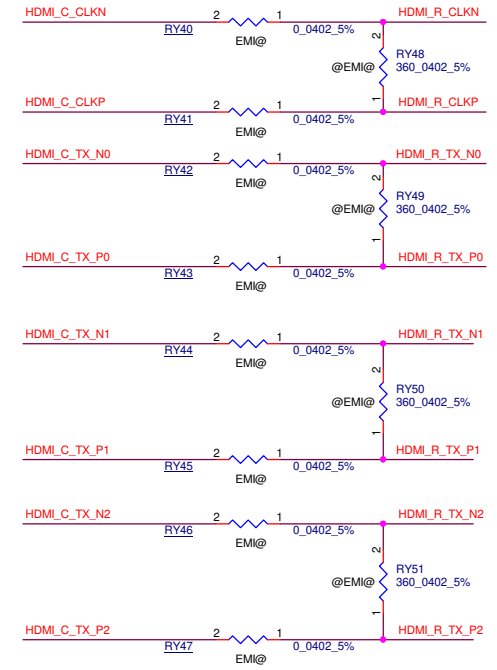
For Camera



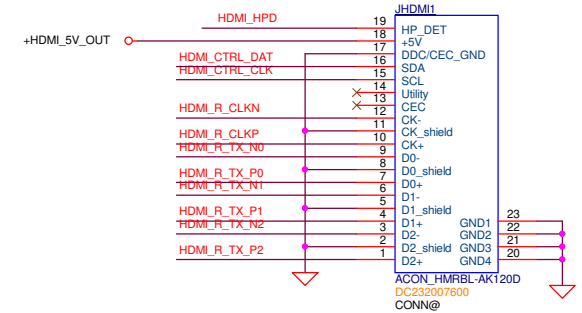
Security Classification		Compal Secret Data		Compal Electronics, Inc.				
Issued Date	2019/03/21	Deciphered Date	2019/03/21	Title				
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				eDP Connector				
				Size	Document Number			Rev
				Custom	EH7L4 M/B LA-J151P			0.1
				Date:	Wednesday, June 19, 2019	Sheet	21	of



P/N: SC300001G00,S DIO(BR) AZC099-04S.R7G SOT23 ESD



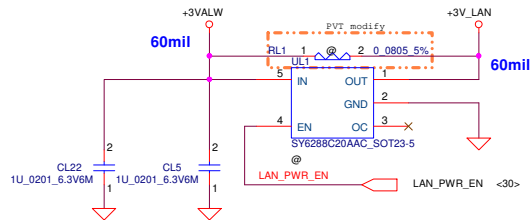
## HDMI connector



SYMBOL: DC232004700

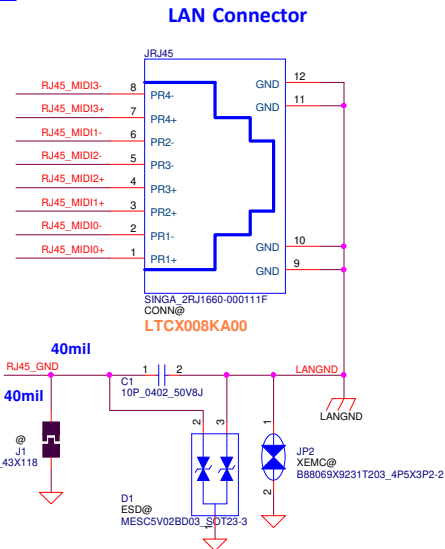
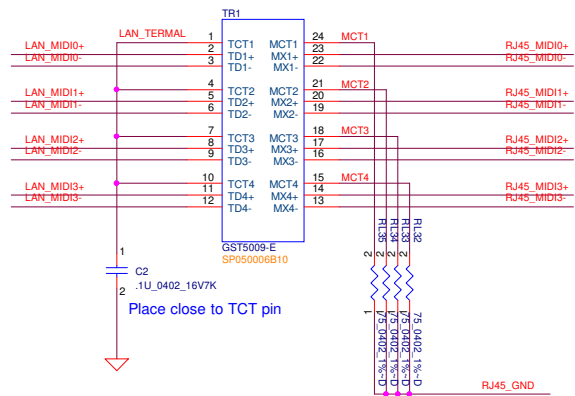
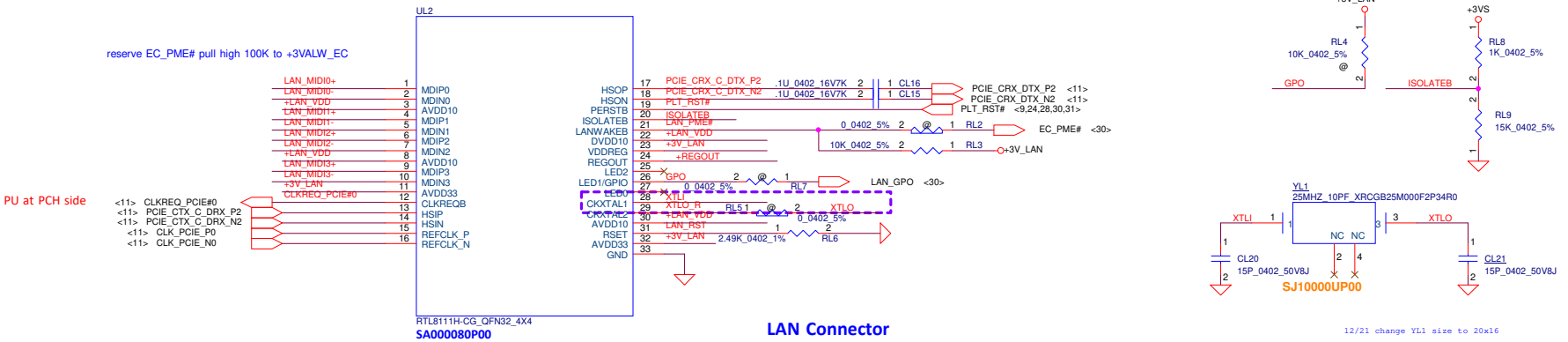
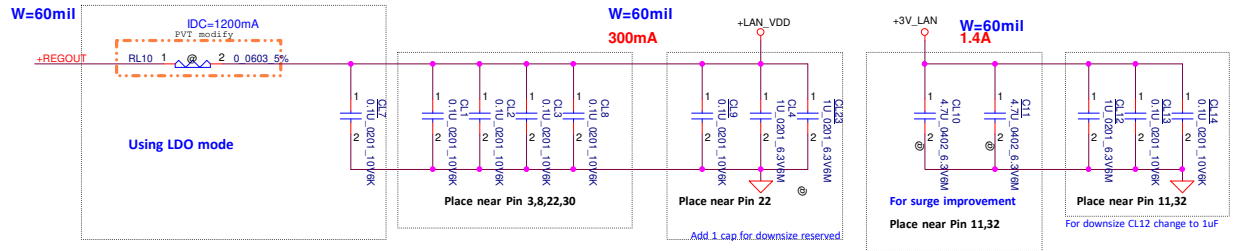
Security Classification		Compal Secret Data				Compal Electronics, Inc.					
Issued Date		2019/03/21		Deciphered Date		2019/03/21		Title			
<p>THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&amp;D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.</p>						HDMI CONN.					
						Size		Document Number		Rev	
						EH7L4 M/B LA-J151P				0.1	
						Date:		Wednesday, June 19, 2019		Sheet 22 of 46	

## LAN-RTL8111H



High active  
EN threshold voltage min:1.2V typ:1.6V max:2.0V  
Current limit threshold 1.5~2.8A

+3V\_LAN Rising time must >0.5 ns and <100 ns

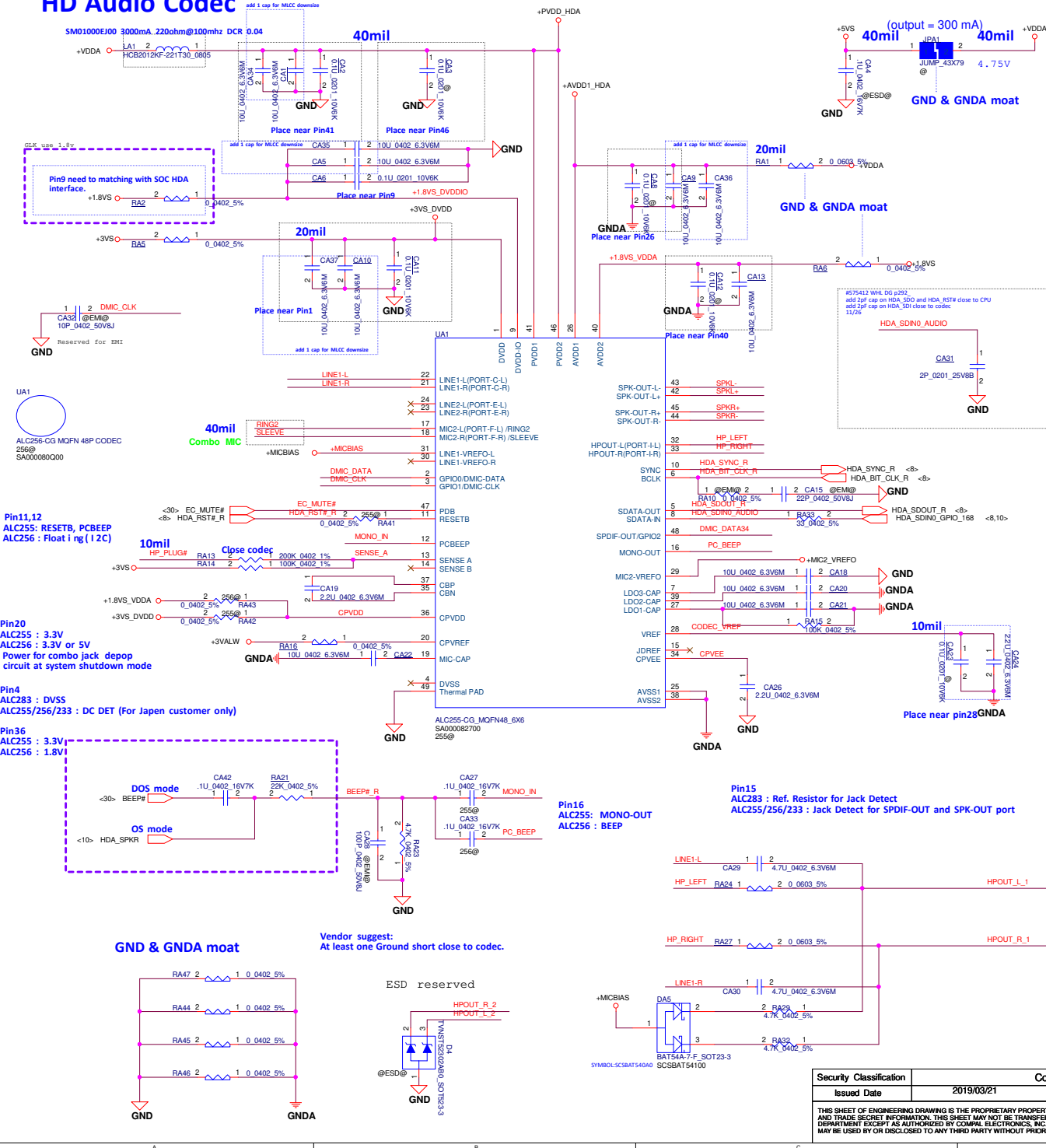


Security Classification		Compal Secret Data		<b>Compal Electronics, Inc.</b> <b>LAN RTL8111H-CG</b>	
Issued Date	2019/03/21	Deciphered Date	2019/03/21	Title	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Rev
				Custom	0.1
				Date: <u>EH714 M/B LA-J151P</u> Date: <u>Wednesday, June 19, 2019</u> Sheet <u>23</u> of <u>48</u>	

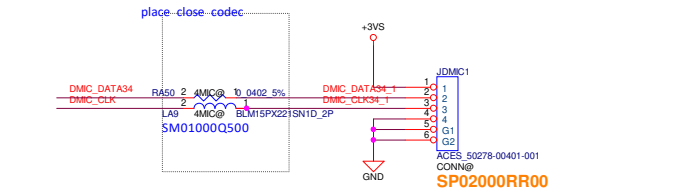
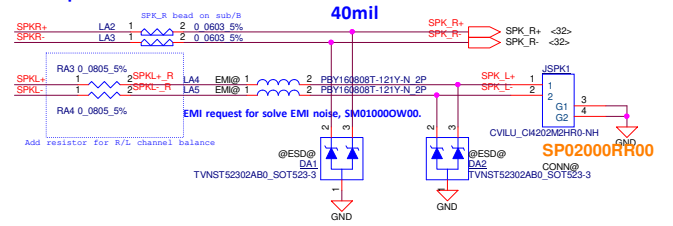




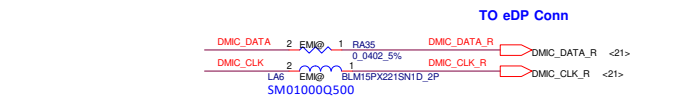
## HD Audio Codec



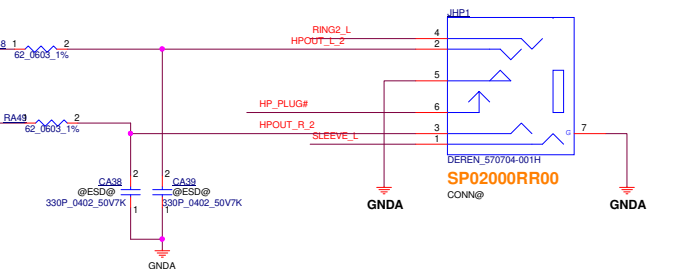
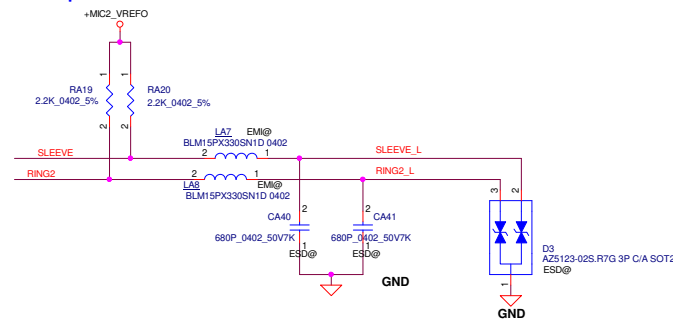
Int. Speaker Conn.



## Digital MIC

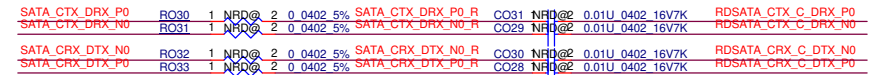
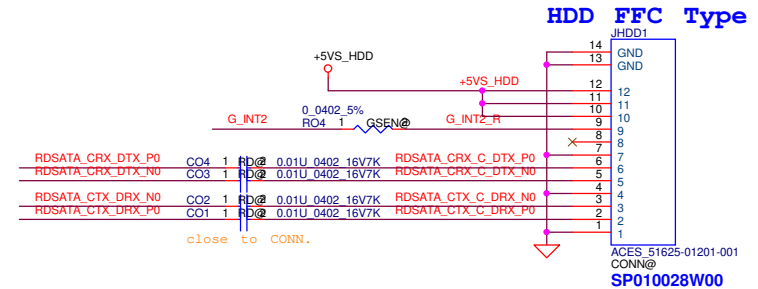
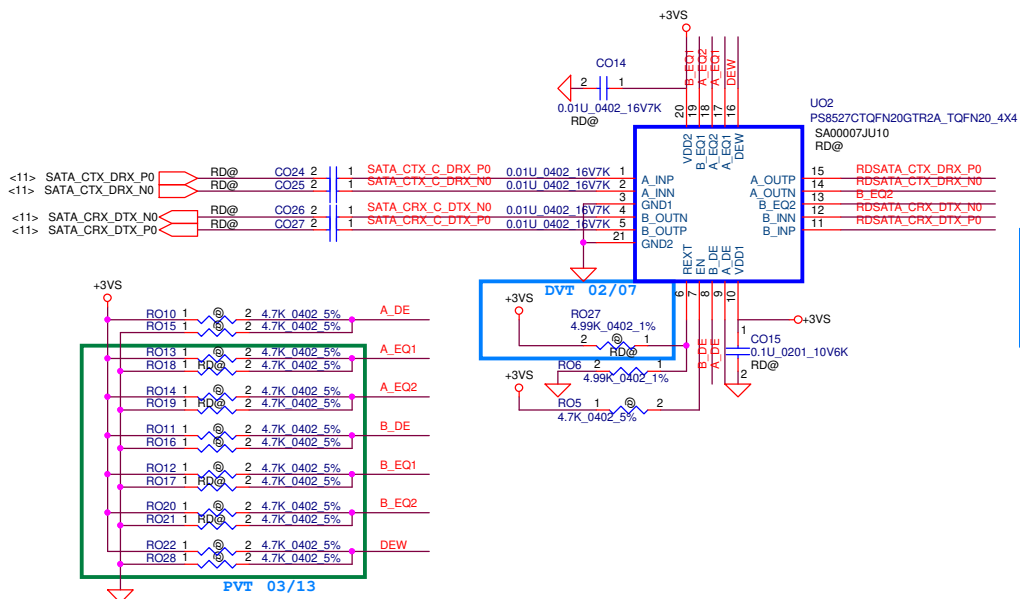
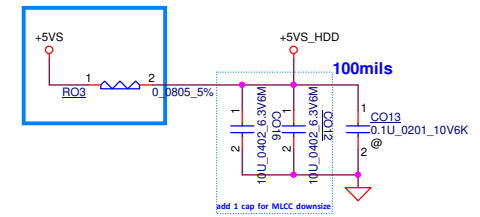
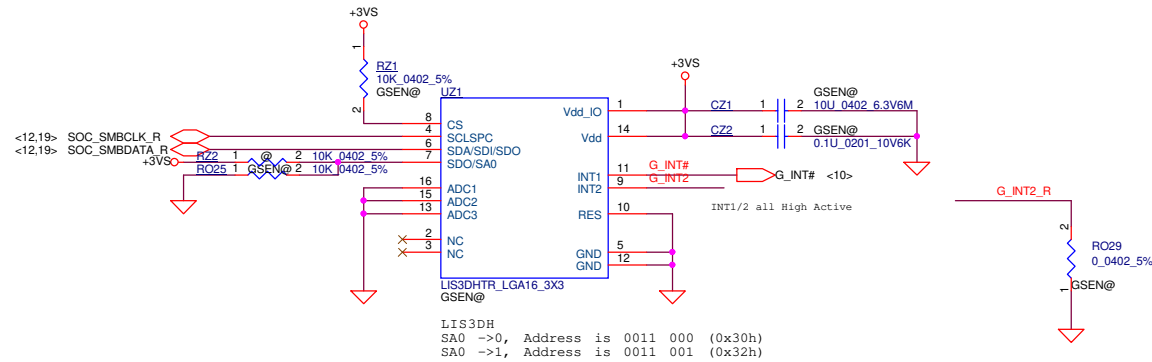


## Headphone Out



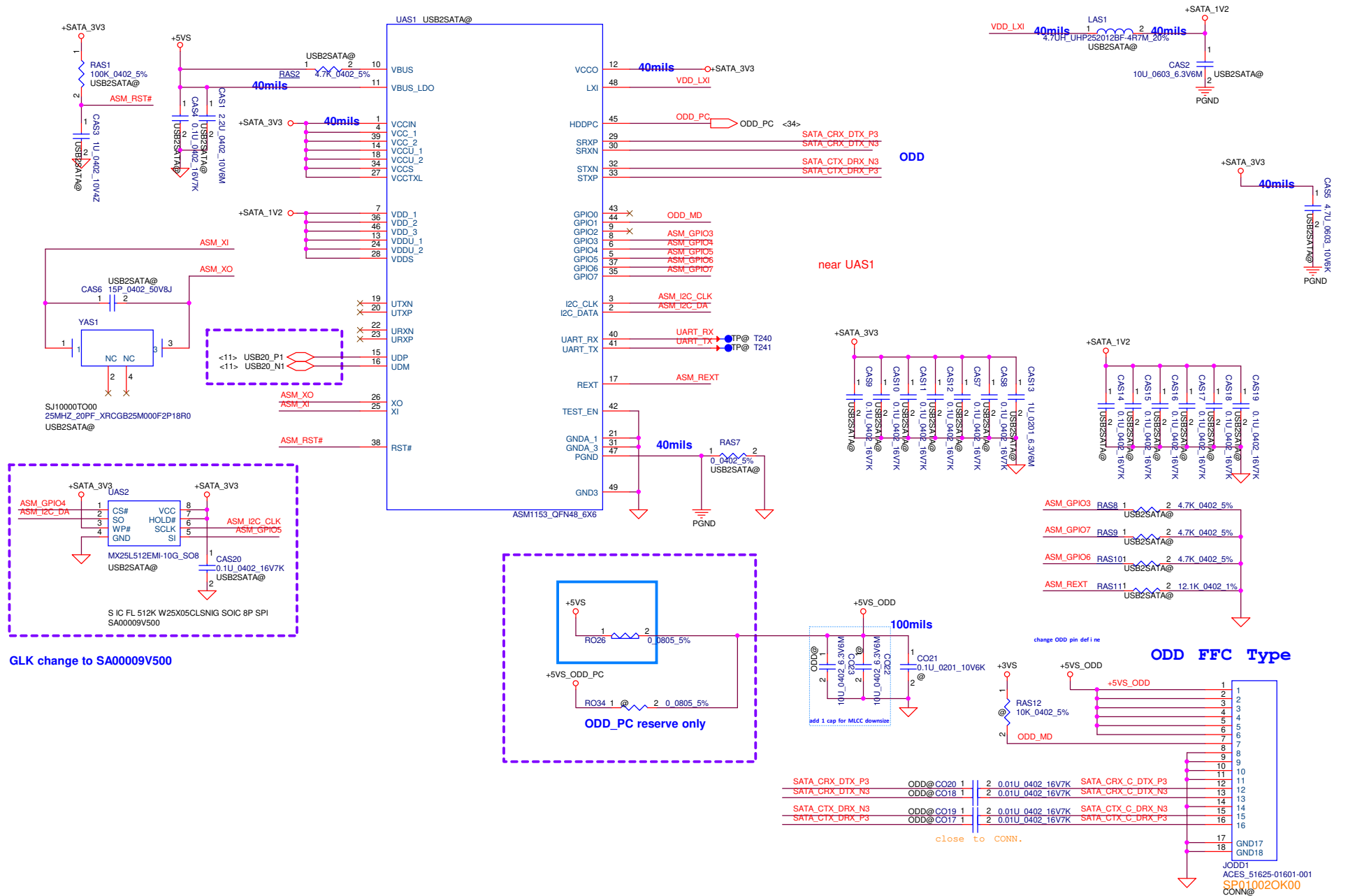
Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2019/03/21	Deciphered Date	2019/03/21	Title	HD Audio Codec ALC255/ALC256 Colay
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number
				Custom	EH7L4 M/B LA-J151P
				Date:	Wednesday June 19, 2019
				Sheet	25 of 46

## G-Sensor reserved for BA serial



Co-lay non redriver

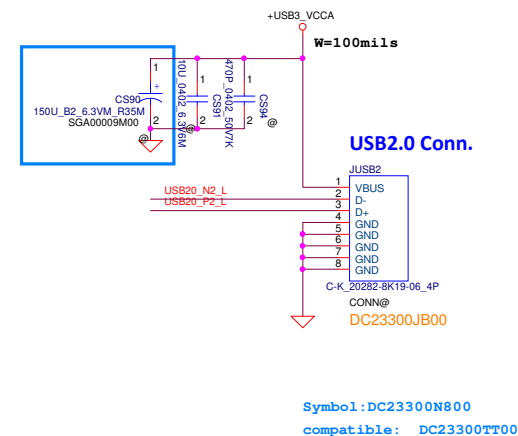
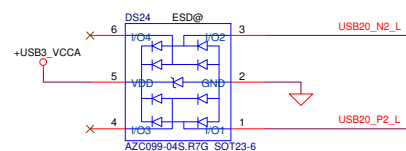
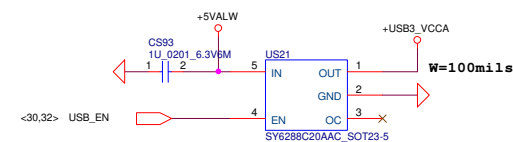
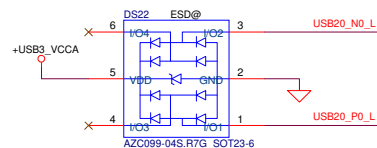
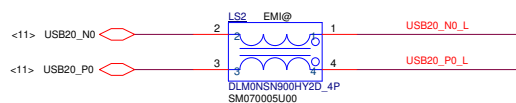
Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2019/03/21	Deciphered Date	2019/03/21	Title	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				G-Sensor/HDD/ODD	
				Size	Rev
				Customer	0.1
Date:	Wednesday, June 19, 2019	Sheet	26	of	46
				EH7L4 MB LA-J151P	



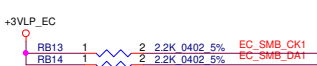
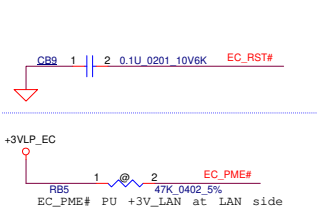
Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2019/03/21	Deciphered Date	2019/03/21	Title	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				USB to SATA ASM1153	
				Size	Number
				Custom	Rev
				EH7L4 M/B LA-J151P	0.1
Date: Wednesday, June 19, 2019		Sheet		27	of 55



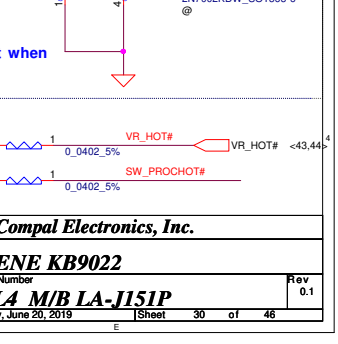
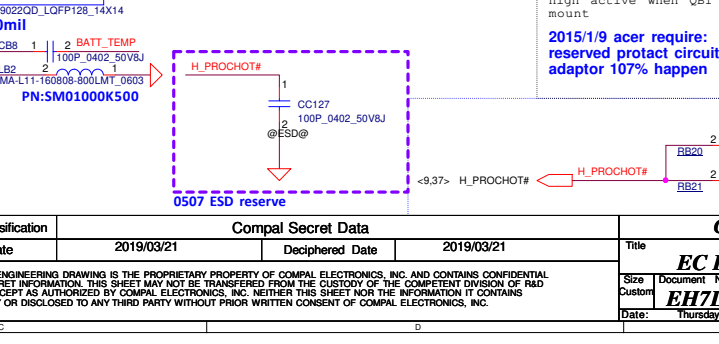
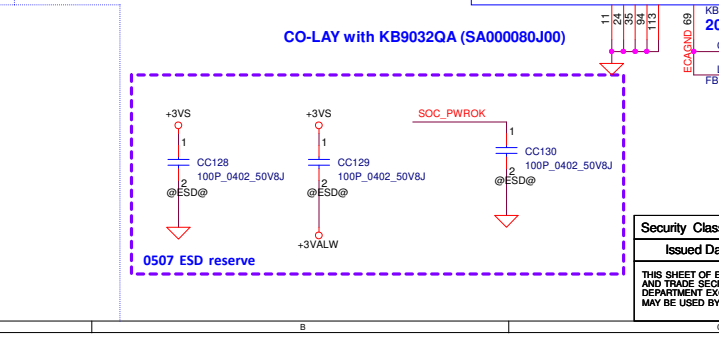
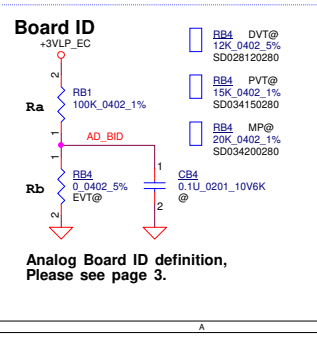
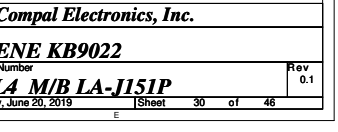
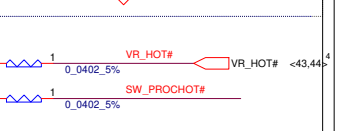
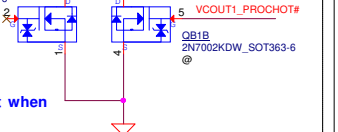
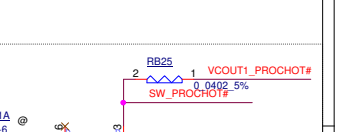
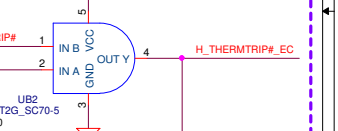
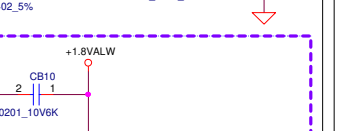
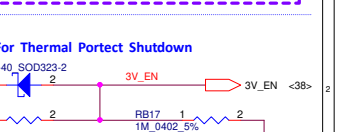
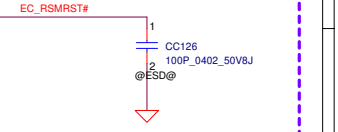
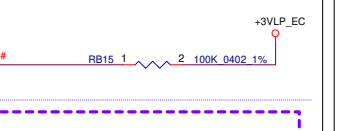
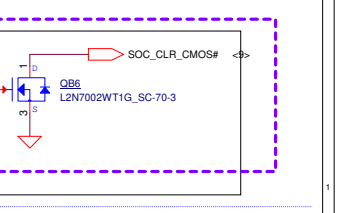
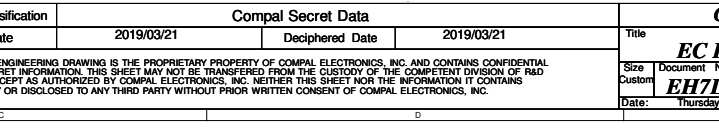
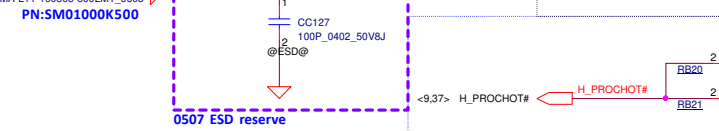
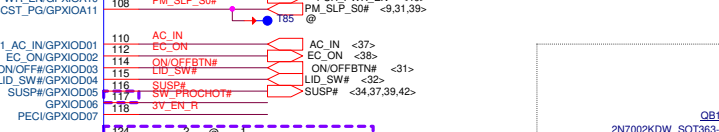
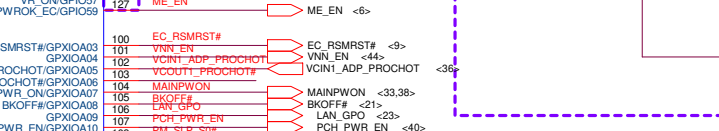
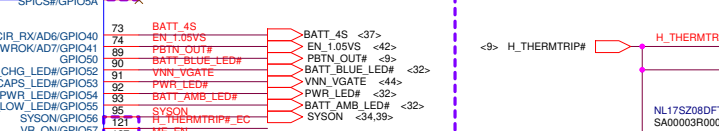
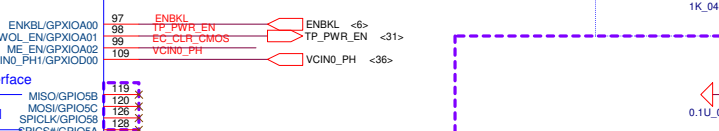
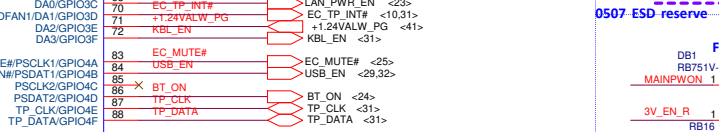
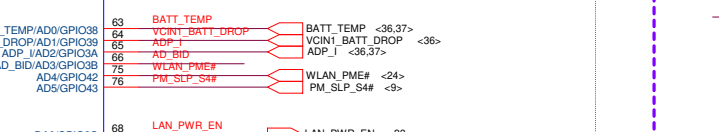
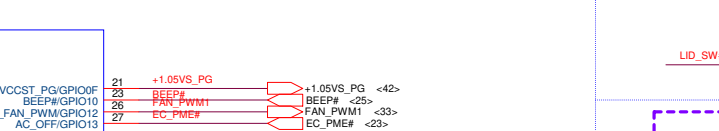
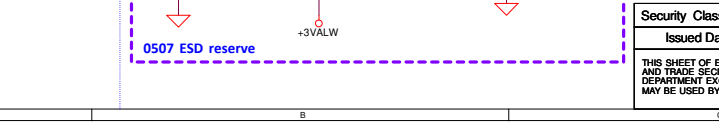
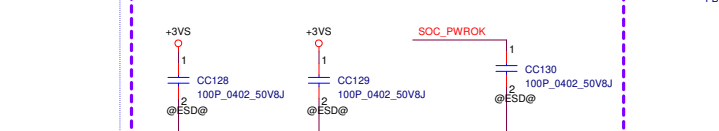
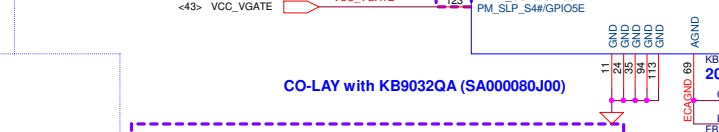
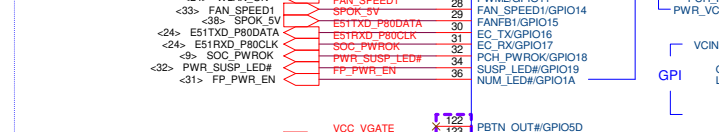
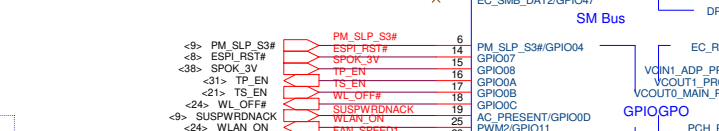
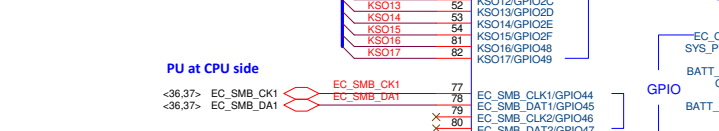
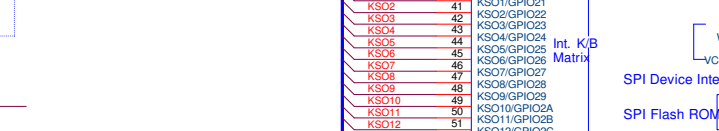
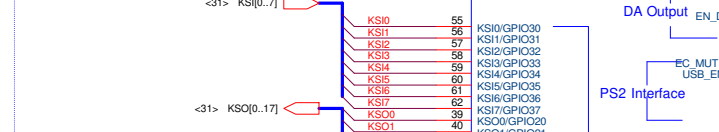
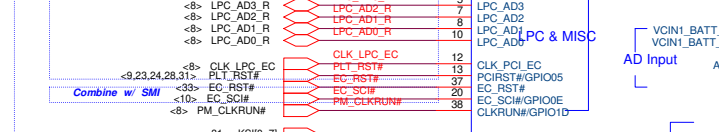
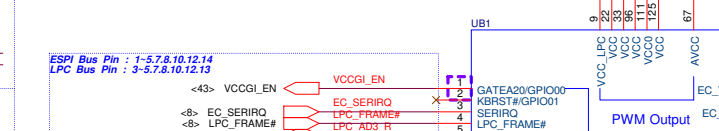
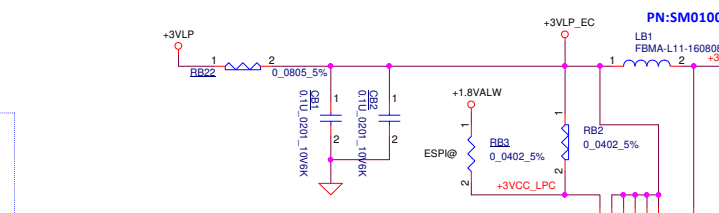
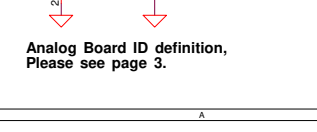
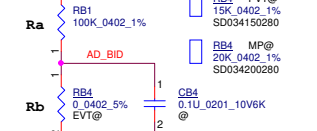
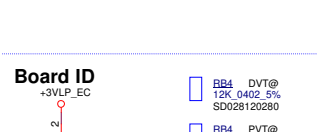
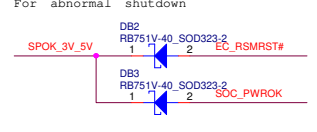
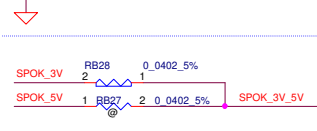
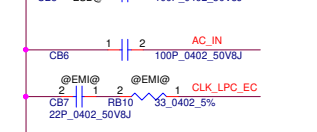
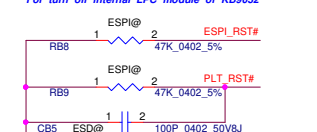
### USB2.0 (Port 2)



Security Classification		Compal Secret Data		Compal Electronics, Inc.		
Issued Date	2019/03/21	Deciphered Date	2019/03/21	Title		
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				USB3 P1 P2 MB		
				Size	Document Number	Rev
				EH7LA M/B LA-1151P		0.1
				Date	Wednesday, June 19, 2019	Sheet 29 of 46



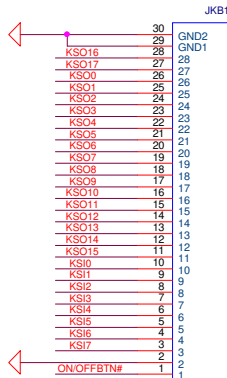
For turn off internal LPC module of KB9032



Security Classification		Compal Secret Data		Title	
Issued Date		Deciphered Date		EC ENE KB9022	
2019/03/21		2019/03/21		EH714 M/B LA-J151P	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.		Size		Rev	
		Document		0.1	
Date:		Thursday, June 20, 2019		Sheet	
		30		of 48	

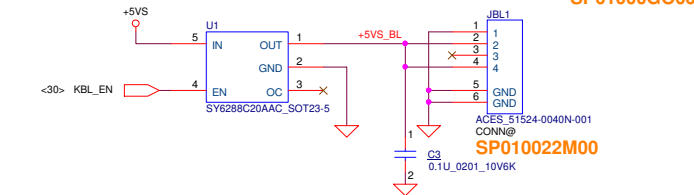
## KB Conn.

### ON/OFF BTN



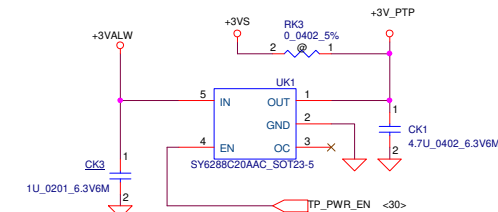
ACES\_85201-2805  
CONN@  
SP01000G000

## KB BackLight

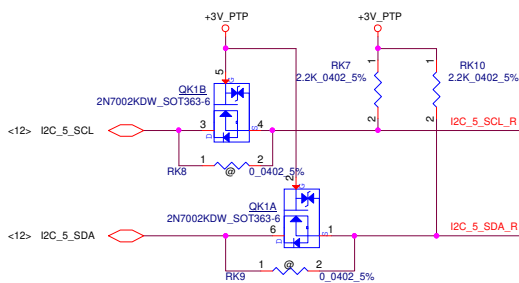


ACES\_51524-0040N-001  
CONN@  
SP010022M00

## TP/B Conn.



TP\_PWR\_EN follow SYSON behavior



EC PS2

PCH I2C

<10,30> EC\_TP\_INT#

<30> TP\_EN

<30> TP\_CLK

<30> TP\_DATA

<30> TP\_CLK

<30> TP\_DATA

<30> TP\_CLK

<30> TP\_DATA

<30> TP\_CLK

<30> TP\_DATA

<30> TP\_CLK

<30> TP\_DATA

<30> TP\_CLK

<30> TP\_DATA

<30> TP\_CLK

<30> TP\_DATA

<30> TP\_CLK

<30> TP\_DATA

<30> TP\_CLK

<30> TP\_DATA

<30> TP\_CLK

<30> TP\_DATA

<30> TP\_CLK

<30> TP\_DATA

<30> TP\_CLK

<30> TP\_DATA

<30> TP\_CLK

<30> TP\_DATA

<30> TP\_CLK

<30> TP\_DATA

<30> TP\_CLK

<30> TP\_DATA

<30> TP\_CLK

<30> TP\_DATA

<30> TP\_CLK

<30> TP\_DATA

<30> TP\_CLK

<30> TP\_DATA

<30> TP\_CLK

<30> TP\_DATA

<30> TP\_CLK

<30> TP\_DATA

<30> TP\_CLK

<30> TP\_DATA

<30> TP\_CLK

<30> TP\_DATA

<30> TP\_CLK

<30> TP\_DATA

<30> TP\_CLK

<30> TP\_DATA

<30> TP\_CLK

<30> TP\_DATA

<30> TP\_CLK

<30> TP\_DATA

<30> TP\_CLK

<30> TP\_DATA

<30> TP\_CLK

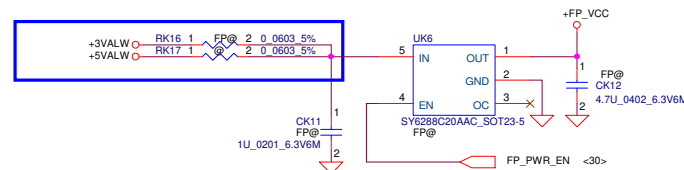
<30> TP\_DATA

## Finger Print

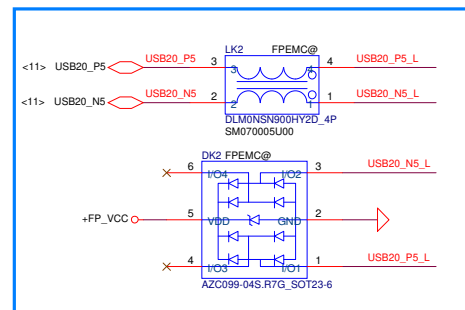
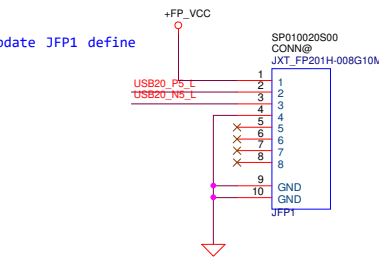
### Power Source Check

EGIS ETU801 +FP\_VCC=5V

ELAN SA464K-2200 +FP\_VCC=3.3V

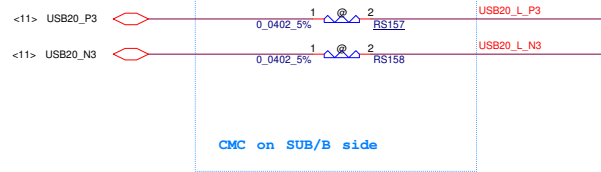


DVT:update JFP1 define



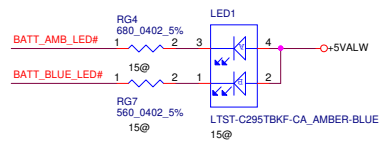
Security Classification			Compal Secret Data			Compal Electronics, Inc.		
Issued Date			Deciphered Date			Title		
2019/03/21			2019/03/21			KB & TP & TPM & FP		
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.			Size			Number		
			Custom			EH714 M/B LA-J151P		
			Date:			Wednesday, June 19, 2019		
			Sheet			31 of 48		
			Rev			0.1		

## USB2 I/O

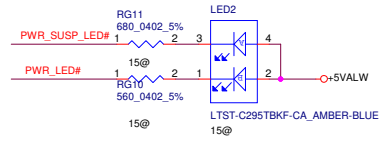


## LED for 15" UMA

### Battery LED



### Power LED

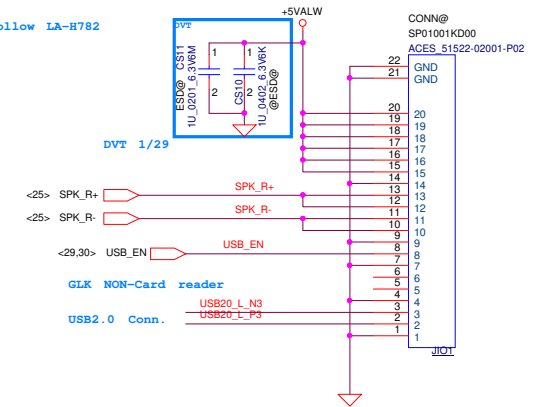


## LID for 15" DIS

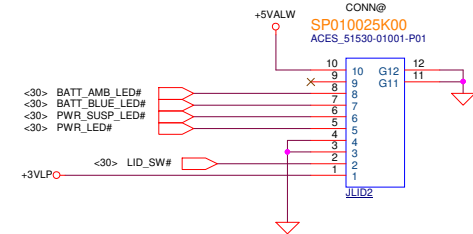
MB LID SW remove on UMA SKU

## USB/B (USBx1,Card Reader,SPK)

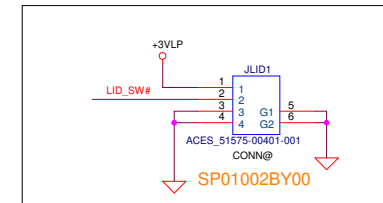
0410 change J101 follow LA-H782



## LID/B with LED for 17" UMA & DIS



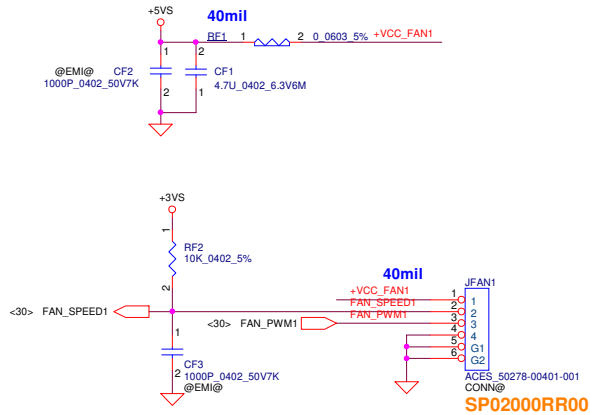
## LID/B for 15" UMA 4pin



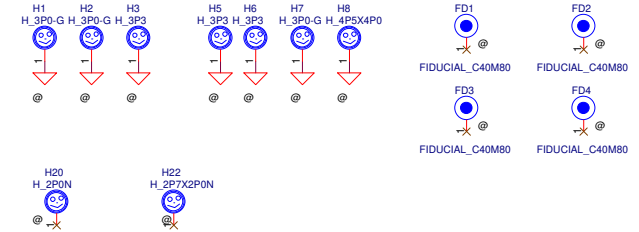
Security Classification	Compal Secret Data			Compal Electronics, Inc.		
Issued Date	2019/03/21	Deciphered Date	2019/03/21	Title	IO/LiD/LED	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number	Rev
				Custom	EH714 M/B LA-J151P	0.1
				Date:	Wednesday, June 19, 2019	Sheet 32 of 48



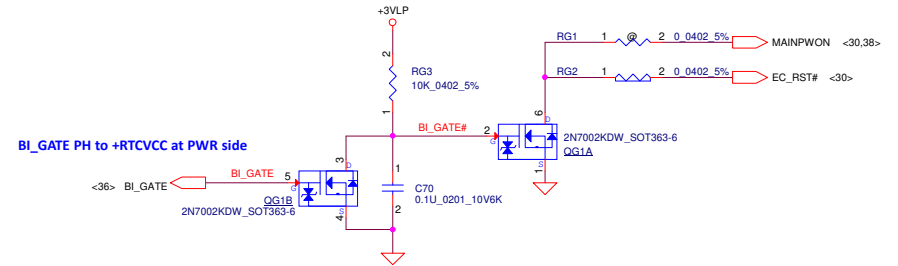
## FAN Conn



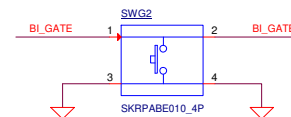
## Screw Hole



## Reset Circuit

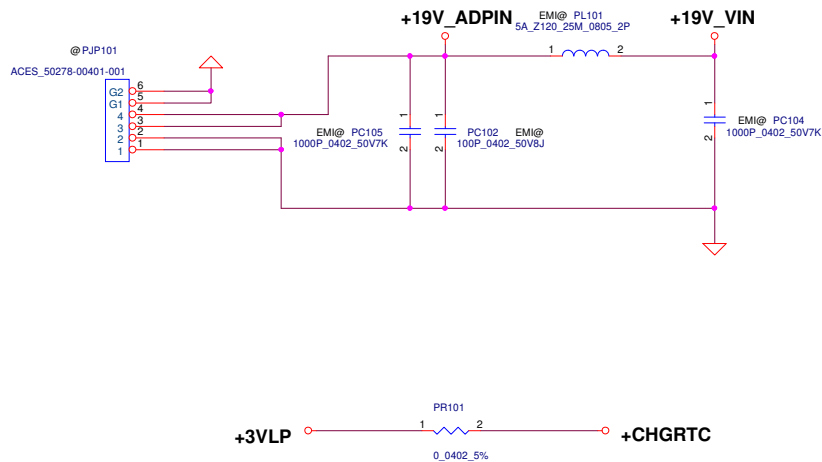


## Reset Button

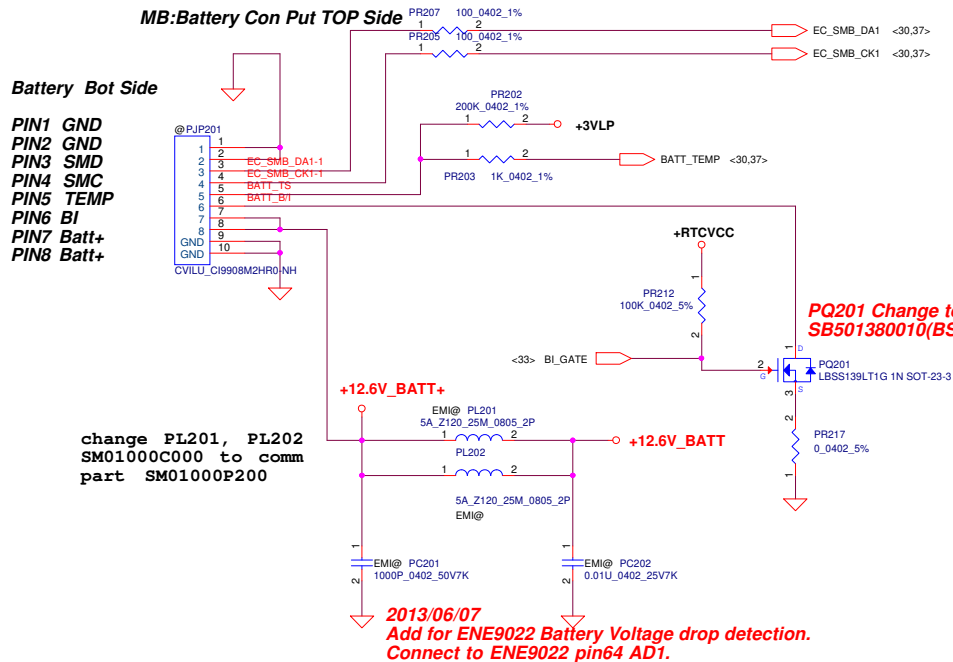


Security Classification	Compal Secret Data			Compal Electronics, Inc.	
Issued Date	2019/03/21	Deciphered Date	2019/03/21	Title	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				FAN & Screw Hole & Reset	
				Size	Rev
				Document Number	0.1
				Custom	
				EH7L4 M/B LA-J151P	
				Date:	Wednesday, June 19, 2019
				Sheet	33 of 48





Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2018/12/27	Deciphered Date	2019/12/27	Title <b>PWR DCIN / Pre-charge</b>	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size Custom	Rev 0.1
				Date: Wednesday, June 18, 2019	Sheet 35 of 35

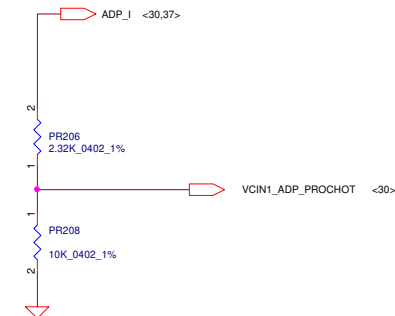
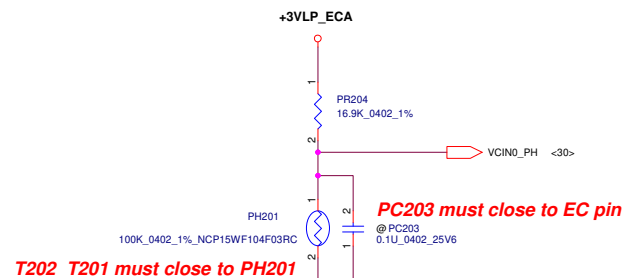
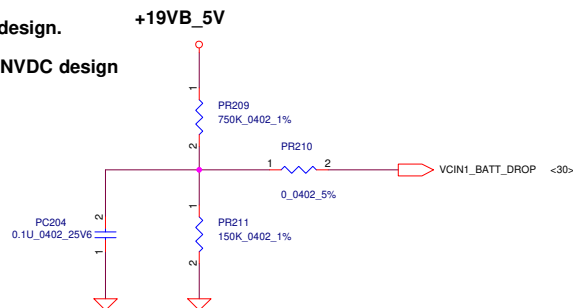


**2016/11/16 update**

For KB9022 sense 20mΩ	Active	Recovery
45W PR206 2.32K ohm	58.5W, 1V	Active=recovery
65W PR206 7.87K ohm	84.5W, 1V	Active=recovery
90W PR20K ohm	__W, __V	Active=recovery
PH1	2V	1V

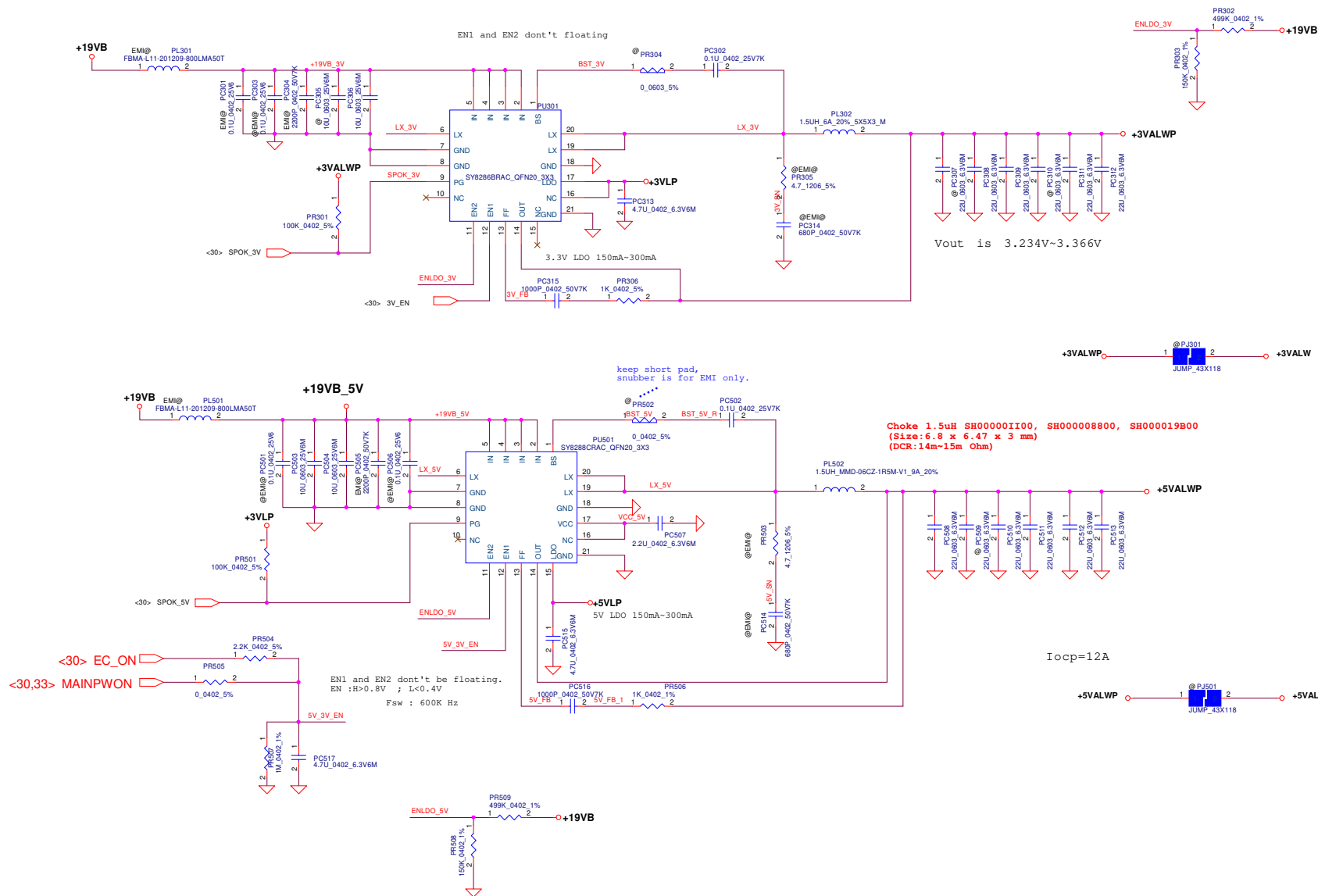
**PH1 under CPU botten side :  
CPU thermal protection at 89 +3 degree C  
Recovery at 56 +3 degree C**

**VAL50/ZAL20 Battery is 3-cell NVDC design.  
B+=9V  
Change PR12=50k if Battery is 2-cell NVDC design  
B+=6V**



Security Classification	Compal Secret Data		Title	
Issued Date	2018/12/27	Deciphered Date	2019/12/27	Compal Electronics, Inc.
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size Document Number
				Custom
				EH7LW M/B LA-H792P
				Date: Wednesday, June 18, 2019
				Sheet 36 of 36
				Rev 0.1



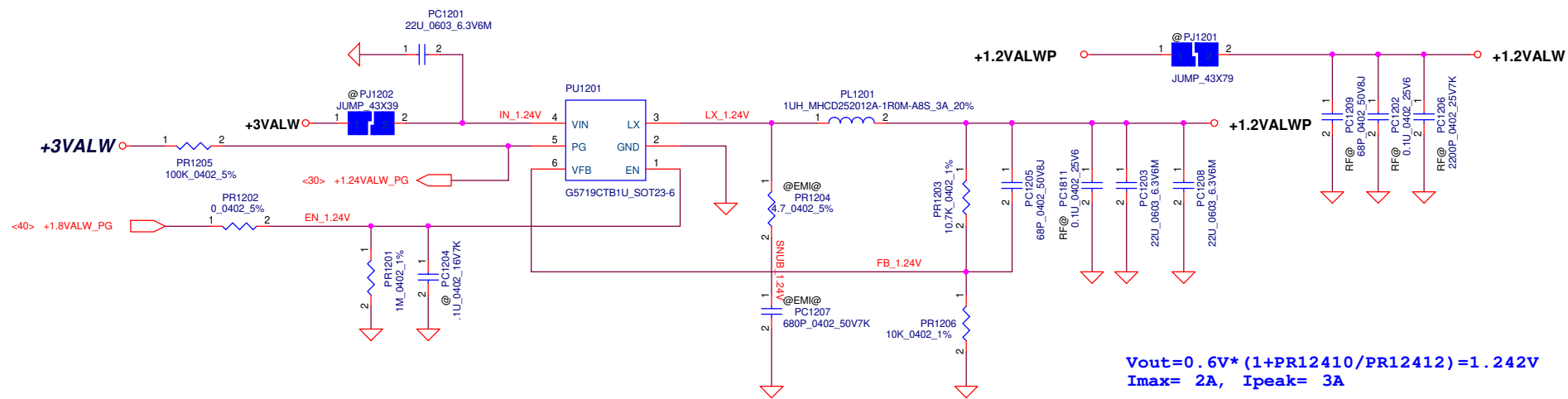


Security Classification		Compal Secret Data		Compal Electronics, Inc.									
Issued Date		2018/12/27		Deciphered Date		2019/12/27		Title					
								+3VALW/+5VALW					
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.								Size		Document Number		Rev	
								Cust#m		EH7LW M/B LA-H792P		0.1	
								Date:		Wednesday, June 19, 2019		Sheet 38 of 38	









Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2017/09/30	Deciphered Date	2019/09/30	Title	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				1.24VALWP	
				Size B	Rev 0.1
Date: Wednesday, June 19, 2019				Sheet 41 of 41	



# Module model information

RT3601EA\_VCCGI\_V2A.mdd for IC portion.  
RT3601EA\_VCCGI\_V2B.mdd for SW portion.

SET1 connect to 5V is into test mode.  
The output is 1.05V.

Local sense, for debug only.  
Trace is form output cap that is near choke.

VCCSENSE and VSSSENSE need have a 100ohm at HW Side

Local sense, for debug only.

Confirm HW side.  
Don't double pull high.

PR802 and PR804 pull high resistor are pop at the end of VR SVID.  
Other VR is unpop.

SVID\_ALERT# pull high resistor is at HW side.

+1.05VS\_SVID

+1.05VS

+19VB\_CPU

+19VB

7x7x3  
0.9m +5%

PL802  
0.15UH\_NA\_35A\_20%

+VCCGI

ISENSE1P\_VCCGI

ISENSE1P\_VCCGI\_R1

ISENSE1P\_VCCGI\_R

ISENSE1P\_VCCGI\_R

ISENSE1P\_VCCGI\_R

ISENSE1P\_VCCGI\_R

ISENSE1P\_VCCGI\_R

ISENSE1P\_VCCGI\_R

ISENSE1P\_VCCGI\_R

ISENSE1P\_VCCGI\_R

ISENSE1P\_VCCGI\_R

ISENSE1P\_VCCGI\_R

ISENSE1P\_VCCGI\_R

ISENSE1P\_VCCGI\_R

ISENSE1P\_VCCGI\_R

ISENSE1P\_VCCGI\_R

ISENSE1P\_VCCGI\_R

ISENSE1P\_VCCGI\_R

ISENSE1P\_VCCGI\_R

ISENSE1P\_VCCGI\_R

ISENSE1P\_VCCGI\_R

ISENSE1P\_VCCGI\_R

ISENSE1P\_VCCGI\_R

ISENSE1P\_VCCGI\_R

ISENSE1P\_VCCGI\_R

ISENSE1P\_VCCGI\_R

ISENSE1P\_VCCGI\_R

ISENSE1P\_VCCGI\_R

ISENSE1P\_VCCGI\_R

ISENSE1P\_VCCGI\_R

ISENSE1P\_VCCGI\_R

ISENSE1P\_VCCGI\_R

ISENSE1P\_VCCGI\_R

ISENSE1P\_VCCGI\_R

ISENSE1P\_VCCGI\_R

ISENSE1P\_VCCGI\_R

ISENSE1P\_VCCGI\_R

ISENSE1P\_VCCGI\_R

ISENSE1P\_VCCGI\_R

ISENSE1P\_VCCGI\_R

ISENSE1P\_VCCGI\_R

ISENSE1P\_VCCGI\_R

ISENSE1P\_VCCGI\_R

ISENSE1P\_VCCGI\_R

ISENSE1P\_VCCGI\_R

ISENSE1P\_VCCGI\_R

ISENSE1P\_VCCGI\_R

ISENSE1P\_VCCGI\_R

ISENSE1P\_VCCGI\_R

ISENSE1P\_VCCGI\_R

ISENSE1P\_VCCGI\_R

ISENSE1P\_VCCGI\_R

ISENSE1P\_VCCGI\_R

ISENSE1P\_VCCGI\_R

ISENSE1P\_VCCGI\_R

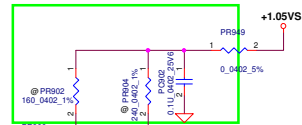
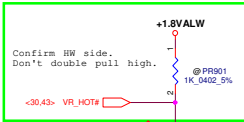
Security Classification		Compal Secret Data	
Issued Date	2011/06/13	Deciphered Date	2012/06/13
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSMITTED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.		Title	
		RT3601EA VCCGI	
Rev	0.1	Docum Number	Custom
Date	Wednesday, June 16, 2011	Sheet	43 of 43

# Module model information

RT3601EA\_VNN\_V2A.mdd for IC portion.  
RT3601EA\_VNN\_V2B.mdd for SW portion.

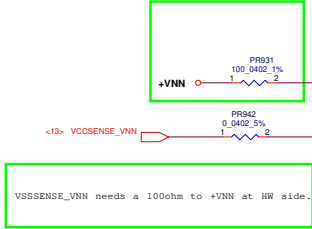
PR902 and PR904 pull high resistor are pop at the end of VR\_SVID.  
Other VR is unpop.

SVID\_ALERT# pull high resistor is at HW side.



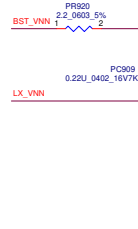
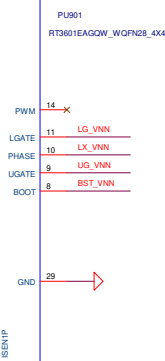
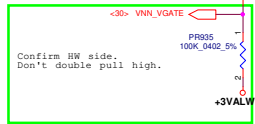
SET1 connect to 5V is into test mode.  
The output is 1.05V.

Local sense, for debug only.  
Trace is from output cap that is near choke.

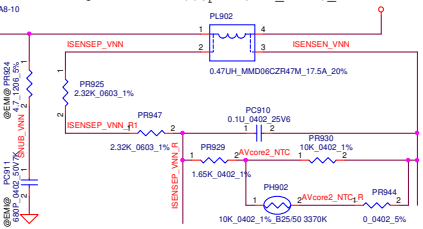


Local sense, for debug only.

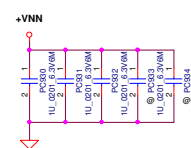
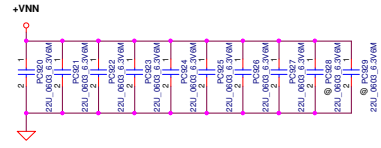
Confirm HW side.  
Don't double pull high.



DCR Typ 3.5m-4m max 4.2m  
Footprint L\_7X7X3\_M



For NTC trace routing only.



Security Classification		Compal Secret Data	
Issued Date	2011/06/13	Deciphered Date	2012/06/13
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSMITTED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.			
Title		RT3601EA VNN	
Rev	Document Number	Rev	0.1
Date	Wednesday, June 18, 2013	Sheet	44 of 44

HW Schematic chang list (P.I.R)

Date	Rev.	Modify Item	Date	Rev.	Modify Item
1/22	0.1				
1/29	0.1				
2/12	0.1				

HW Schematic chang list (P.I.R)

Item    Page    Date    Rev.    Reason for change    Modify Item

Security Classification		Compal Secret Data		Compal Electronics, Inc.			
Issued Date		Deciphered Date		Title			
2018/12/27		2019/12/27		HW-P.I.R page2			
<small>THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&amp;D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.</small>				Size	Rev		
				Document Number		0.1	
				EH7LW M/B LA-H792P			
Date:		Wednesday, June 19, 2019		Sheet	46 of 46		

Item	Fixed Issue	Reason for change	Rev.	PG#	Modify List	Date	Phase
01							
02							
03							
04							
05							
06							
07							
08							
09							
10							
11							
12							
13							
14							
15							
16							
17							
18							
19							

Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2018/12/27	Deciphered Date	2019/12/27	Title <b>PWR-PIR</b>	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size Custom	Document Number <b>EH7LW M/B LA-H792P</b>
				Rev 0.2	
Date: Wednesday, June 18, 2019				Sheet 47 of 47	